

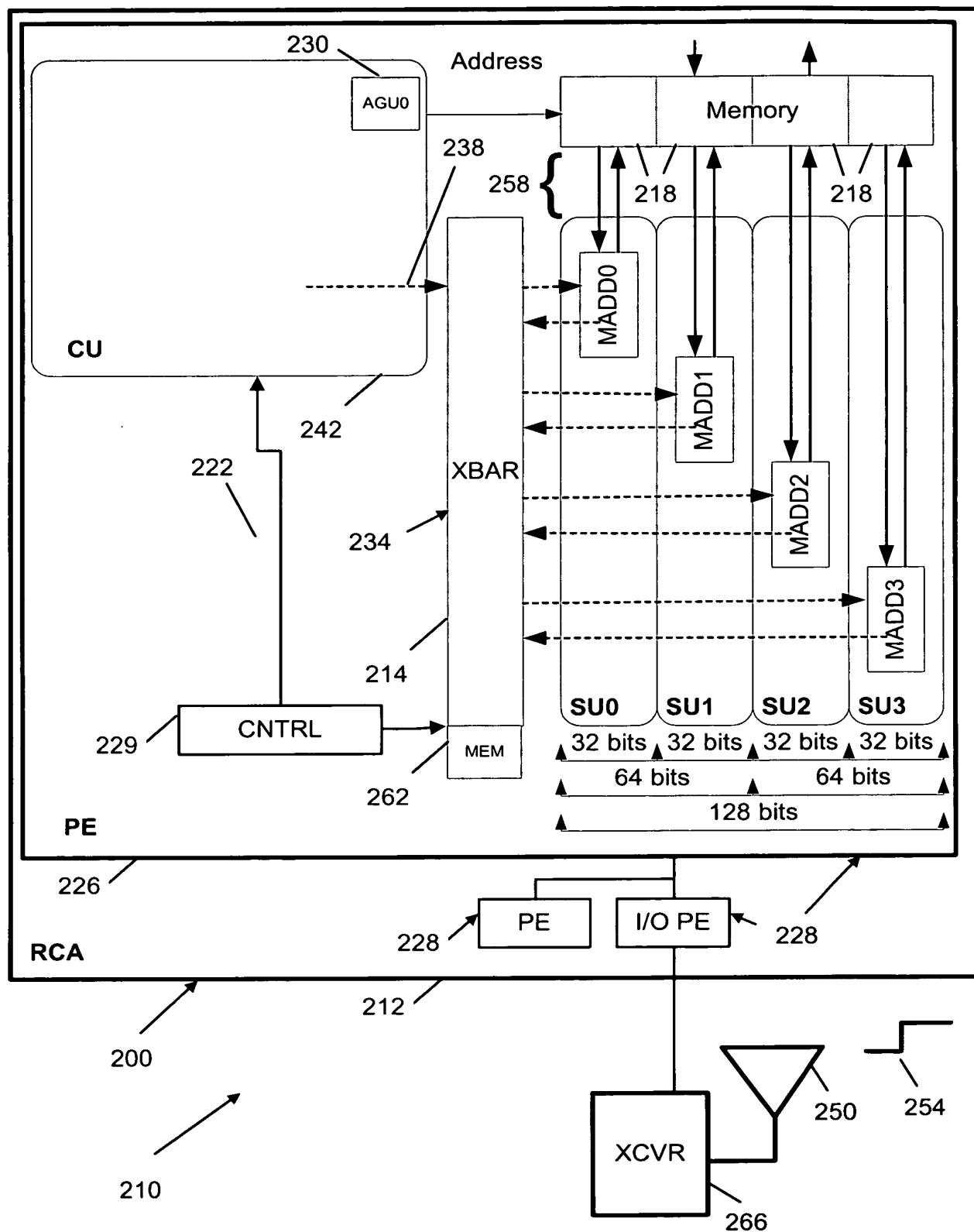
**FIG. 1**

100

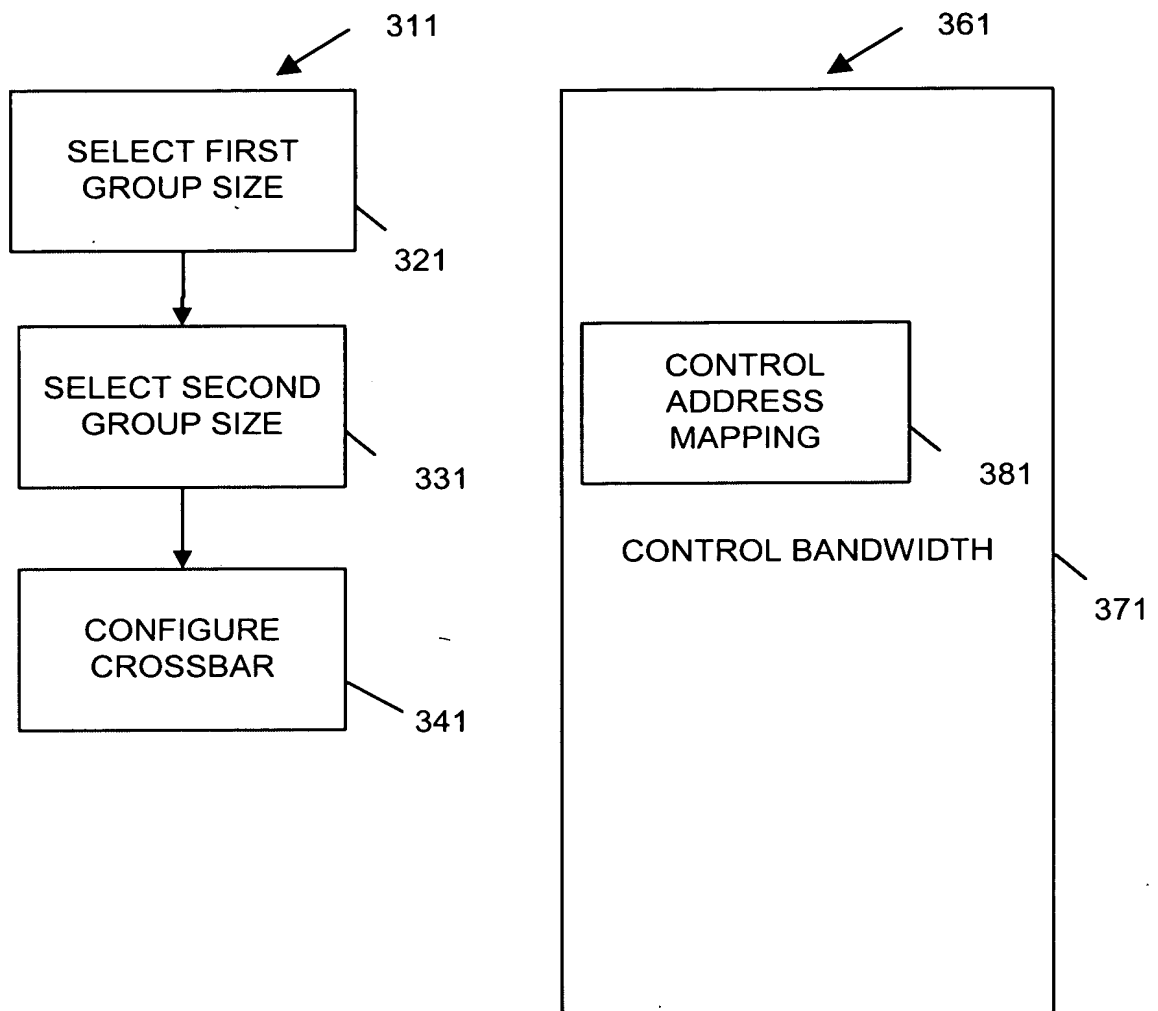


Mode of Accesses	Bank 0	Bank 1	Bank 2	Bank 3
<b>32-bit Access:</b>	0 → 0	0 → 1	0 → 2	0 → 3
	1 → 4	1 → 5	1 → 6	1 → 7
	2 → 8	2 → 9	2 → 10	2 → 11
	3 → 12	3 → 13	3 → 14	3 → 15
<b>64-bit Access:</b>	0 → 0	1 → 1	0 → 2	1 → 3
	2 → 4	3 → 5	2 → 6	3 → 7
	4 → 8	5 → 9	4 → 10	5 → 11
	6 → 12	7 → 13	6 → 14	7 → 15
<b>128-bit Access:</b>	0 → 0	1 → 1	2 → 2	3 → 3
	4 → 4	5 → 5	6 → 6	7 → 7
	8 → 8	9 → 9	10 → 10	11 → 11
	12 → 12	13 → 13	14 → 14	15 → 15

FIG. 2



**FIG. 3**



**FIG. 4**

